## **Specification Amendments:**

At page 1, below the title, please insert the following:

This application is a divisional of patent application serial number 09/910,771, entitled "Method of Preparing Buried LOCOS Collar in Trench DRAMS," filed on July 24, 2001, and this application is a continuation of patent application serial number 10/147,472, entitled "Method of Preparing Buried LOCOS Collar in Trench DRAMS," filed on September 10, 2002, which applications are incorporated herein by reference.

At page 6, after line 17, please insert the following:

## BRIEF DESCRIPTION OF THE DRAWINGS

FIG. 1 is an elevation cross section of a conventional vertical DRAM capacitor cell at an intermediate stage of processing; and

FIGS. 2-12 are elevation cross sections of a DRAM structure in accordance with a preferred embodiment of the invention at various stages of processing.